

Refine Search

Search Results -

Term	Documents
(25 AND 19).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	51
(L25 AND L19).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	51

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L26

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Wednesday, June 02, 2004 [Printable Copy](#) [Create Case](#)

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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
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<u>L25</u>	(skip\$4 or avoid\$3 or bypass\$3 or without) near9 execut\$3 and arithmetic\$3 near12 vector\$1	337	<u>L25</u>
<u>L24</u>	bypass\$3 near5 execut\$3 near6 arithmetic\$3 and vector\$1	0	<u>L24</u>
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<u>L23</u>	L20 and vector\$1 near4 condition\$3 near5 arithmetic\$3	9	<u>L23</u>
<u>L22</u>	L21	9	<u>L22</u>
<u>L21</u>	L20 and vector\$1 near4 condition\$3 near5 arithmetic\$3	9	<u>L21</u>
<u>L20</u>	L19 or l18	22397	<u>L20</u>
<u>L19</u>	(708/101-714)![CCLS]	13111	<u>L19</u>
<u>L18</u>	(712/2-300)![CCLS]	9847	<u>L18</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		

<u>L17</u>	L16 not l15	4	<u>L17</u>
<u>L16</u>	L14 and (without or skip\$4 or eliminat\$3 or avoid\$3 or determin\$7 or decid\$3 or decision\$1) near6 execut\$3	48	<u>L16</u>
<u>L15</u>	L14 and (without or skip\$4 or eliminat\$3 or avoid\$3 or determin47) near6 execut\$3	44	<u>L15</u>
<u>L14</u>	L13 not l4	57	<u>L14</u>
<u>L13</u>	L12 and vector\$1 near8 arithmetic\$3	57	<u>L13</u>
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<u>L9</u>	L5 and vector\$1	179	<u>L9</u>
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<u>L6</u>	L5 and target near4 (data or operand\$1)	106	<u>L6</u>
<u>L5</u>	(decid\$3 or decision\$1 or determin\$7 or check\$3) near12 arithmetic\$3 near6 execut\$3	1130	<u>L5</u>
<u>L4</u>	L3 and vector\$1	49	<u>L4</u>
<u>L3</u>	L1 and target near4 (data or operand\$1)	72	<u>L3</u>
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<u>L1</u>	(decid\$3 or decision\$1 or determin\$7 or check\$3) near6 arithmetic\$3 near4 execut\$3	617	<u>L1</u>

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Fwd Refs

Bkwd Refs

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Search Results - Record(s) 1 through 20 of 26 returned.

☐ 1. Document ID: US 20030200237 A1

Using default format because multiple data bases are involved.

L34: Entry 1 of 26

File: PGPB

Oct 23, 2003

PGPUB-DOCUMENT-NUMBER: 20030200237

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030200237 A1

TITLE: Serial operation pipeline, arithmetic device, arithmetic-logic circuit and operation method using the serial operation pipeline

PUBLICATION-DATE: October 23, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Naoi, Junichi	Tokyo		JP	

US-CL-CURRENT: 708/233

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 2. Document ID: US 20010009011 A1

L34: Entry 2 of 26

File: PGPB

Jul 19, 2001

PGPUB-DOCUMENT-NUMBER: 20010009011

PGPUB-FILING-TYPE: new-utility

DOCUMENT-IDENTIFIER: US 20010009011 A1

TITLE: Digital signal processor and digital signal processing system incorporating same

PUBLICATION-DATE: July 19, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Sogo, Akira	Hamamatsu-shi		JP	

US-CL-CURRENT: 708/490

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 3. Document ID: US 20010004741 A1

L34: Entry 3 of 26

File: PGPB

Jun 21, 2001

PGPUB-DOCUMENT-NUMBER: 20010004741
PGPUB-FILING-TYPE: new-utility
DOCUMENT-IDENTIFIER: US 20010004741 A1

TITLE: Digital signal processor and processing system incorporating

PUBLICATION-DATE: June 21, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Sogo, Akira	Hamamatsu-shi		JP	

US-CL-CURRENT: 708/490; 708/523

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 4. Document ID: US 6732132 B2

L34: Entry 4 of 26

File: USPT

May 4, 2004

US-PAT-NO: 6732132
DOCUMENT-IDENTIFIER: US 6732132 B2

TITLE: Digital signal processor and digital signal processing system incorporating same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 5. Document ID: US 6223197 B1

L34: Entry 5 of 26

File: USPT

Apr 24, 2001

US-PAT-NO: 6223197
DOCUMENT-IDENTIFIER: US 6223197 B1

TITLE: Constant multiplier, method and device for automatically providing constant multiplier and storage medium storing constant multiplier automatic providing program

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 6. Document ID: US 6205459 B1

L34: Entry 6 of 26

File: USPT

Mar 20, 2001

US-PAT-NO: 6205459

DOCUMENT-IDENTIFIER: US 6205459 B1

TITLE: Digital signal processor and digital signal processing system incorporating same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 6202078 B1

L34: Entry 7 of 26

File: USPT

Mar 13, 2001

US-PAT-NO: 6202078

DOCUMENT-IDENTIFIER: US 6202078 B1

TITLE: Arithmetic circuit using a booth algorithm

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 8. Document ID: US 5892696 A

L34: Entry 8 of 26

File: USPT

Apr 6, 1999

US-PAT-NO: 5892696

DOCUMENT-IDENTIFIER: US 5892696 A

TITLE: Pipeline controlled microprocessor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☒ 9. Document ID: US 5636150 A

L34: Entry 9 of 26

File: USPT

Jun 3, 1997

US-PAT-NO: 5636150

DOCUMENT-IDENTIFIER: US 5636150 A

TITLE: Data driven type digital filter unit and data driven type information processor including the same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 10. Document ID: US 5475710 A

L34: Entry 10 of 26

File: USPT

Dec 12, 1995

US-PAT-NO: 5475710

DOCUMENT-IDENTIFIER: US 5475710 A

TITLE: Adaptive equalizer and receiver

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 11. Document ID: US 5465225 A

L34: Entry 11 of 26

File: USPT

Nov 7, 1995

US-PAT-NO: 5465225

DOCUMENT-IDENTIFIER: US 5465225 A

TITLE: Method of increasing the data-processing speed of a signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 12. Document ID: US 5418915 A

L34: Entry 12 of 26

File: USPT

May 23, 1995

US-PAT-NO: 5418915

DOCUMENT-IDENTIFIER: US 5418915 A

TITLE: Arithmetic unit for SIMD type parallel computer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 13. Document ID: US 5386375 A

L34: Entry 13 of 26

File: USPT

Jan 31, 1995

US-PAT-NO: 5386375

DOCUMENT-IDENTIFIER: US 5386375 A

TITLE: Floating point data processor and a method for performing a floating point square root operation within the data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 14. Document ID: US 5148386 A

L34: Entry 14 of 26

File: USPT

Sep 15, 1992

US-PAT-NO: 5148386

DOCUMENT-IDENTIFIER: US 5148386 A

TITLE: Adder-subtracter for signed absolute values

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 15. Document ID: US 4956800 A

L34: Entry 15 of 26

File: USPT

Sep 11, 1990

US-PAT-NO: 4956800

DOCUMENT-IDENTIFIER: US 4956800 A

TITLE: Arithmetic operation processing apparatus of the parallel processing type and compiler which is used in this apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 16. Document ID: US 4947359 A

L34: Entry 16 of 26

File: USPT

Aug 7, 1990

US-PAT-NO: 4947359

DOCUMENT-IDENTIFIER: US 4947359 A

**** See image for Certificate of Correction ****

TITLE: Apparatus and method for prediction of zero arithmetic/logic results

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 17. Document ID: US 4941120 A

L34: Entry 17 of 26

File: USPT

Jul 10, 1990

US-PAT-NO: 4941120

DOCUMENT-IDENTIFIER: US 4941120 A

TITLE: Floating point normalization and rounding prediction circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 18. Document ID: US 4926355 A

L34: Entry 18 of 26

File: USPT

May 15, 1990

US-PAT-NO: 4926355

DOCUMENT-IDENTIFIER: US 4926355 A

TITLE: Digital signal processor architecture with an ALU and a serial processing section operating in parallel

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 19. Document ID: US 4924422 A

L34: Entry 19 of 26

File: USPT

May 8, 1990

US-PAT-NO: 4924422

DOCUMENT-IDENTIFIER: US 4924422 A

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for modified carry-save determination of
arithmetic/logic zero results

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 20. Document ID: US 4888722 A

L34: Entry 20 of 26

File: USPT

Dec 19, 1989

US-PAT-NO: 4888722

DOCUMENT-IDENTIFIER: US 4888722 A

TITLE: Parallel arithmetic-logic unit for as an element of digital signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
(33 NOT 4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	26
(L33 NOT L4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	26

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